

VLSI forum – role, activities, goals

By

S. Nagendra

Senior Marketing Engineer
nagendra@unistring.com

Unistring Tech Solutions Pvt. Ltd

D10, 5th Floor, Eureka court, Beside Image hospitals, Ameerpet,
Hyderabad, ph:040-23732798, 09440318188
www.unistring.com

String Technologies

#309, Vederi complex, opp Dilshuk Nagar bus Depot, Dilshuk nagar, Hyderabad,
ph: 040 – 24151900
www.stringtechnologies.net

- ☐ Aiming to give VLSI industrial exposure to academicians
- ☐ Motivating the academicians to take part in industry level of VLSI projects.
- ☐ Bringing the latest semi-custom VLSI technologies to universities/colleges.
- ☐ Promoting the FPGA technologies at university level, and further bringing education more close to VLSI and embedded industries.
- ☐ A non-commercial activity launched under UTS – University Programme.

Scope

- ☐ VLSI front end design issues
 - ☐ FPGA front end and back end design issues
 - ☐ ASIC circuit level design techniques
 - ☐ ASIC schematic & layout based design techniques and simulation tools
- (Industry standard high end tools)

Scope (continued ...)

- ☐ FPGAs for ASIC prototyping
- ☐ Hands on training sessions over FPGA boards
- ☐ FPGAs for embedded system development
- ☐ Hardware software co-design concepts
- ☐ VLSI testing concepts
- ☐ VHDL, Verilog, system verilog coding styles and issues

Activities

- Weekly 4 hour discussion sessions at our company (2 +2)
- Guest talks
- Case studies from our senior design engineers
- On line access through www.vlsionline.com to participate in webtalks, tool demos etc.

What is expected from participants

- Registration fees Rs 0/-
- Interest and zeal to learn VLSI technologies.
- A bit of homework
- Availability on weekends

Thank you
for
your interest to join with us

and

Wish you all the success
in FPGA and VLSI technologies